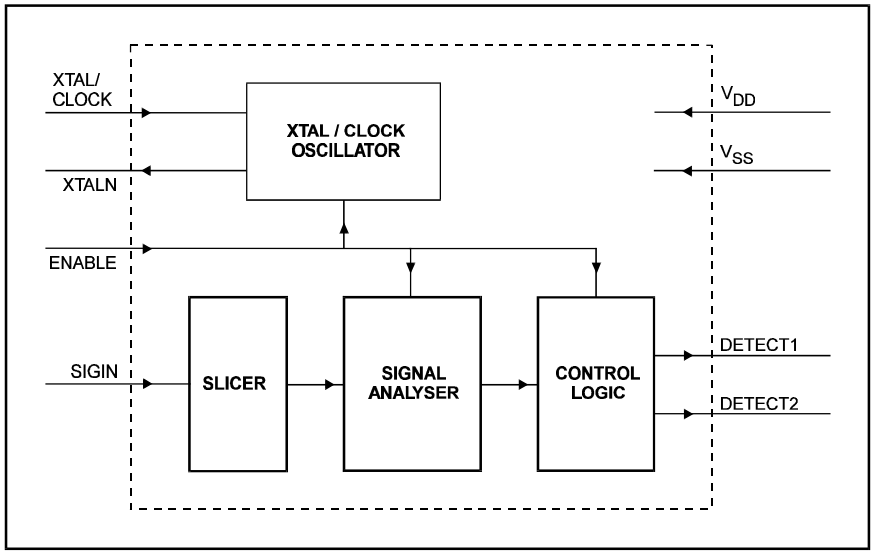
CML Semiconductor Products

Call Progress Tone Detector FX633

D/633/3 March 1996
Provisional Issue

1.0 Features

- **Worldwide Tone Compatibility**
- **Single and Dual Tones Detected**
- **Special US Busy-Detect Output**
- **Special Voice-Detect Output**
- **Wide Dynamic Signal Range**
- **Low Voltage Operation**
- **Standard 8-Pin DIP Package**
- **Standard 3.58MHz Xtal**



1.1 Brief Description

The FX633 detects the audible tone signals used by most of the world's Telecom Systems to indicate Dial, Ringing, Busy and other conditions found when placing a call. Detection of these call progress stages is essential to the proper operation of automatic calling products.

The FX633 adds new features to Call Progress monitoring. It detects and indicates separately the "US Busy" tones, reducing the need to measure "tone cadence" to identify "US Busy". It detects and separately indicates "Voice" and other signals from Call Progress tones, reducing voice-falsing and adding voice-answer as a connection prompt.

The FX633 uses the latest signal processing techniques to provide these advantages. It is a low cost, low power product with superior performance. It is available in the industry standard package.

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1.2 Block Diagram

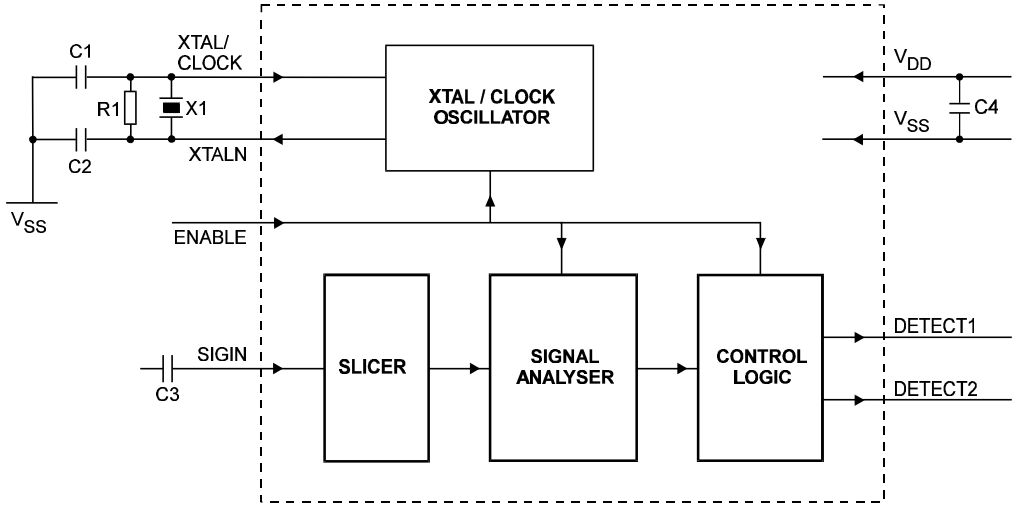


Figure 1 Block Diagram

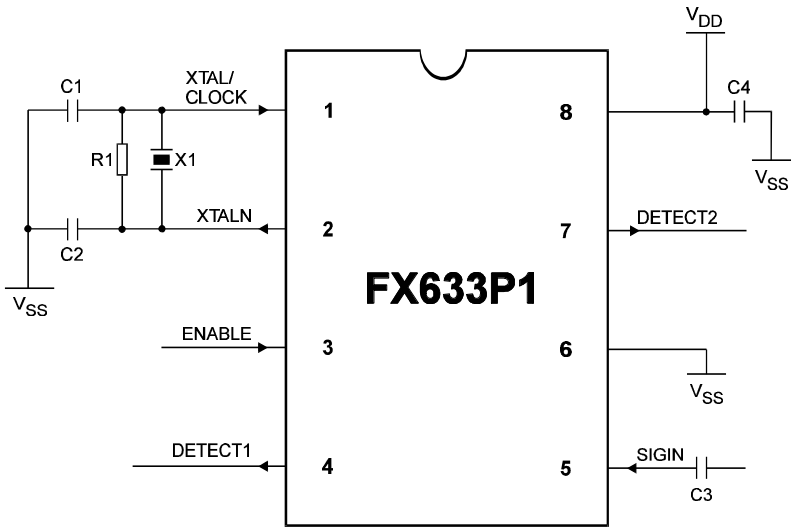
1.3 Signal List

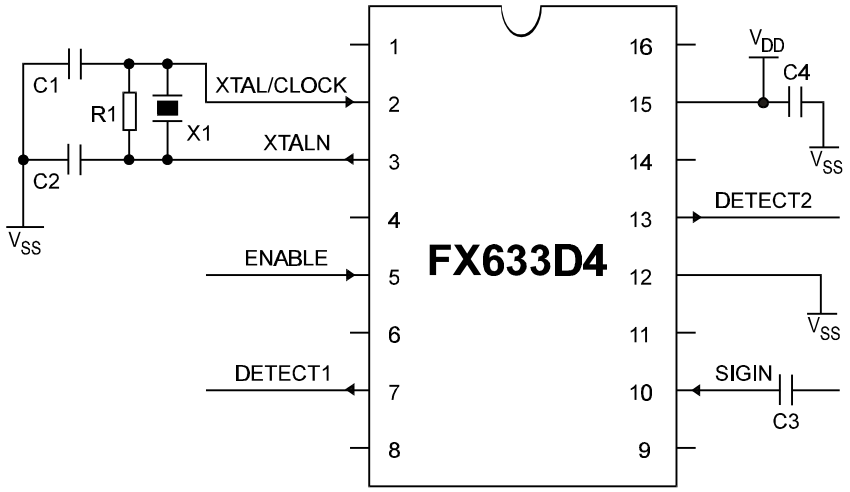
Package D4	Package P1	Signal		Description
Pin No.	**Pin No.**	**Name**	**Type**	
2	1	XTAL/CLOCK	I/P	The input to the on-chip oscillator, for external Xtal circuit or clock.
3	2	XTALN	O/P	The inverted output of the on-chip oscillator.
5	3	ENABLE	I/P	A logic "0" pulse of at least 1µs applied to this input resets the decoder circuits and forces both DETECT1 and DETECT2 outputs to a logic "0".
7	4	DETECT1	O/P	When a call progress signal is detected, this output goes to a logic "1".
10	5	SIGIN	I/P	Signal input. Signals to this pin should be ac coupled. The dc bias of this pin is set internally.
12	6	V_{SS}	Power	The negative supply rail (ground).
13	7	DETECT2	O/P	This output is used in conjunction with DETECT1. When DETECT1 is at a logic "1", this output goes to a logic "1" if a Call Progress High Band signal is detected. When DETECT1 is at a logic "0", this output goes to a logic "1" if a Non Call Progress signal is detected.
15	8	V_{DD}	Power	The positive supply rail. This pin should be decoupled to V_{SS} by a capacitor.
1, 4, 6, 8, 9, 11, 14, 16		NC		Internal Connection. Do not make any connection to these pins.

Notes: I/P = Input
O/P = Output

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1.4 External Components





Typical Values:

R1	1MΩ	± 10%
C1	33pF	± 20%
C2	33pF	± 20%
C3	1nF	± 20% at 5V, 560pF ± 20% at 3.3V.
C4	0.1µF	± 20%
X1	3.579545MHz	(refer to Section 1.7.1)

Figure 2 Recommended External Components

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1.5 General Description

1.5.1 Overall Function Description

The FX633 Call Progress Tone Detector uses different tone detection methods from those commonly found with other products.

Many traditional devices use a bandpass filter followed by an energy detector. The filter is usually designed to pass input signals with a frequency between about 300Hz and 700Hz, and the amplitudes of signals in this range are then checked against a level threshold. Any signal of acceptable level in this frequency band is classed as a Call Progress tone, including signals due to speech and noise. False outputs caused by speech are a common feature with these products, and background noise may lead to a stuck "detect" output.

The FX633, by contrast, uses a stochastic signal processing technique based on analysis in both the frequency and time domain, with signal amplitude forming a small part in the decision process. This analysis includes checks on whether the signal has a "profile" which matches international standards for Call Progress tones, or a profile more likely to match that of speech, noise or other non-call-progress signals. It also adds checks on whether tones which include frequencies corresponding with the "US Busy" signals have been detected.

The following Glossary, and the Decode Truth Table in section 1.5.4, provide a simple explanation of the decoding functions and features offered by the FX633.

1.5.2 Glossary

Call Progress Tones: The single and dual frequency tones in the range 350Hz to 620Hz specified widely for call progress signalling.

Call Progress Band: The nominal range 340Hz to 650Hz within which the FX633 will detect Call Progress tones. The detection algorithm requires that the tones have the characteristics typical of Call Progress Tones.

Call Progress Low Band: The nominal range 340Hz to 490Hz. The FX633 will detect single or dual tones falling entirely within this range as Call Progress Low Band tones.

Call Progress High Band: The nominal range 600Hz to 650Hz. Single tones in this range, or dual tones having a material frequency component within this range (e.g. 480 + 620Hz), are detected as Call Progress High Band tones.

Non Call Progress Signal: A signal falling within the nominal range (a) 200Hz to 800Hz, but NOT within the Call Progress Band, or (b) within the nominal range 200Hz to 800Hz, but NOT meeting the DETECTION REQUIREMENTS when the signal falls in the Call Progress Band.

Subject to the duration and other characteristics of such signals, the FX633 will usually interpret these as a Non Call Progress Signal (e.g. speech or other signal activity).

Minimum Input Signal: The minimum signal level for the specified tone decoding performance. The lower level at which absence of an input signal will be registered is not specified.

No Signal: A signal falling outside the nominal range 120Hz to 900Hz or the absence of an input signal. Either will be detected as a No Signal condition.

Nominal: Subject to dynamic tolerances within the signal analysis process. Absolute values are not material or adverse to performance.

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1.5.3 Block Diagram Description

(reference Figure 1)

Slicer

The input signal to the slicer is amplified by a self-biased inverting amplifier. The dc bias of this input is internally set at $\frac{1}{2}V_{DD}$.

Signal Analyser

The frequency range, quality and consistency of the input signal is analysed by this functional block. To be classified as a call progress signal the input signal frequencies should lie between 340Hz and 650Hz. The signal to noise ratio must be 16dB or greater. The signal must be consistent over a period of about 140 ms. These decode criteria are continuously monitored and the assessment is updated every 7 ms, reference Figure 3.

The analyser samples the call progress signal at 9.3kHz; so care should be taken to avoid high frequency signals (e.g. 18kHz) aliasing into the call progress band.

Control Logic

This block categorises the nature of the signal into various decoded output states and controls the two output pins. See the Decode Output Truth Table in section 1.5.4.

Xtal/Clock Oscillator

If the on-chip Xtal oscillator is to be used, then external components X1, R1, C1 and C2 are required. If an external clock source is to be used, then it should be connected to the XTAL/CLOCK input pin and the XTALN pin should be left unconnected.

1.5.4 Decode Output Truth Table

DETECT2	DETECT1	CONDITIONS
0	0	No Signal
0	1	Call Progress Low Band: Will detect 350+440, 400+450, 440+480, 400, 425, 440, and 450Hz tones
1	1	Call Progress High Band: Will detect 480+620, 600 and 620Hz tones
1	0	Non Call Progress signal, e.g. Voice Activity

Note that DETECT1 responds to the whole range of call progress tones from 340Hz to 650Hz.

1.6 Application Notes

1.6.1 General

On power-up, a logic "0" at the ENABLE input may be used to disable and initialise the device.

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1.7 Performance Specification

1.7.1 Electrical Performance

Absolute Maximum Ratings
Exceeding these maximum ratings can result in damage to the device.

	Min.	Max.	Units
Supply (V_{DD} - V_{SS})	-0.3	7.0	V
Voltage on any pin to V_{SS}	-0.3	V_{DD} + 0.3	V
Current into or out of V_{DD} and V_{SS} pins	-30	+30	mA
Current into or out of any other pin	-20	+20	mA

P1 Package	**Min.**	**Max.**	**Units**
Total Allowable Power Dissipation at Tamb = 25°C		800	mW
... Derating		13	mW/°C
Storage Temperature	-55	+125	°C
Operating Temperature	-40	+85	°C

D4 Package	**Min.**	**Max.**	**Units**
Total Allowable Power Dissipation at Tamb = 25°C		800	mW
... Derating		13	mW/°C
Storage Temperature	-55	+125	°C
Operating Temperature	-40	+85	°C

Operating Limits
Correct operation of the device outside these limits is not implied.

	Notes	**Min.**	**Max.**	**Units**
Supply (V_{DD} - V_{SS})		3.0	5.5	V
Xtal Frequency		3.57	3.59	MHz

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Operating Characteristics
For the following conditions unless otherwise specified:

Xtal Frequency = 3.579545MHz, S/N = 16 dB, Noise Bandwidth = 5 kHz,
V_{DD} = 3.3V to 5.0V, Tamb = - 40°C to +85°C. 0dB = 775mVrms.

	Notes	Min.	Typ.	Max.	Units
DC Parameters					
I_{DD} (ENABLE = "1") (V_{DD} = 5.0V)	1		0.5	1.0	mA
I_{DD} (ENABLE = "1") (V_{DD} = 3.3V)	1		0.3	0.7	mA
AC Parameters					
SIGIN pin					
Input Impedance	2		0.35		MΩ
Minimum Input Signal Level	3		-40		dB
Input Signal Dynamic Range	3	40			dB
Signal to Noise Ratio		16			dB
Xtal/Clock Input					
'High' Pulse Width	4	40			ns
'Low' Pulse Width	4	40			ns
Input Impedance (at 100Hz)		10			MΩ
Gain (I/P = 1mV rms at 100Hz)		20			dB
Logic Interface					
Input Logic "1" Level	5	80%			V_{DD}
Input Logic "0" Level	5			20%	V_{DD}
Input Leakage Current (Vin = 0 to V_{DD})	5	-5.0		+5.0	µA
Input Capacitance	5		10.0		pF
Output Logic "1" Level (I_{OH} = 120µA)	6	90%			V_{DD}
Output Logic "0" Level (I_{OL} = 360µA)	6			10%	V_{DD}

Notes:
1. Not including any current drawn from the device pins by external circuitry.
2. Small signal impedance over the frequency range 100Hz to 2000Hz and at 5.0V.
3. The input level is not critical as the detector uses a stochastic algorithm.
4. Timing for an external input to the XTAL/CLOCK pin.
5. ENABLE pin.
6. DETECT1 and DETECT2 pins.

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1.7.1 **Electrical Performance** (continued)

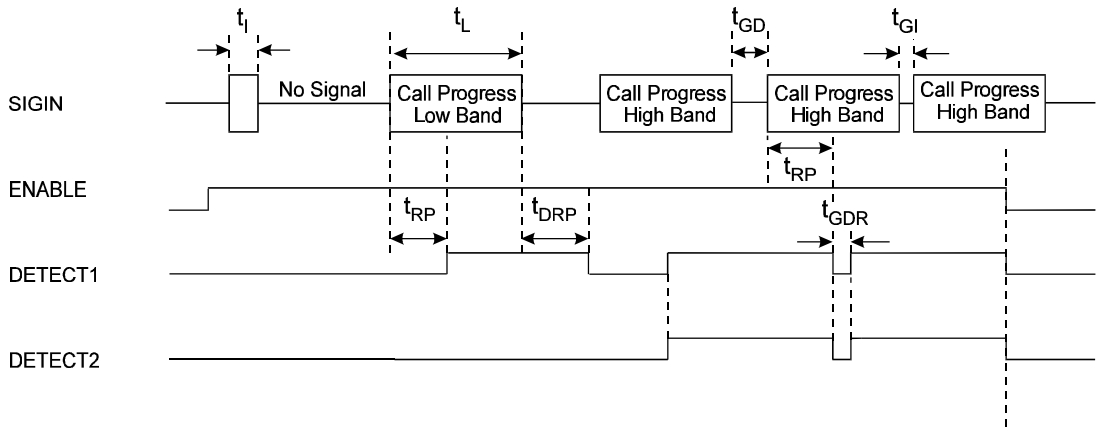


Figure 3 Timing Diagram: Call Progress Tone(s)

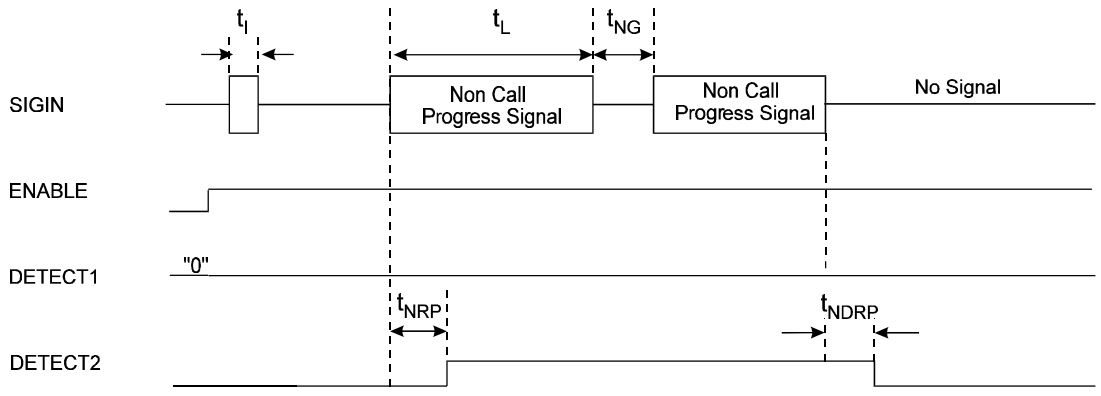


Figure 4 Timing Diagram: Non Call Progress Signal

1.7.1 **Electrical Performance** (continued)

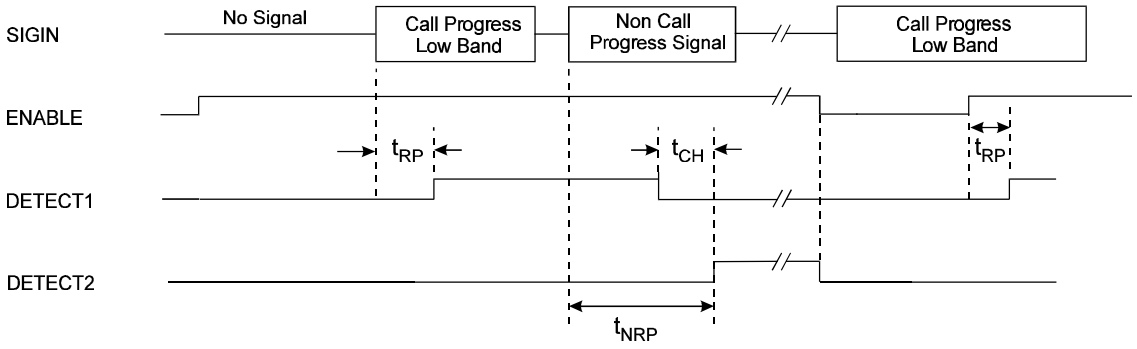


Figure 5 Timing Diagram: Call Progress Tone(s) to Non Call Progress Signal

For the following conditions unless otherwise specified:

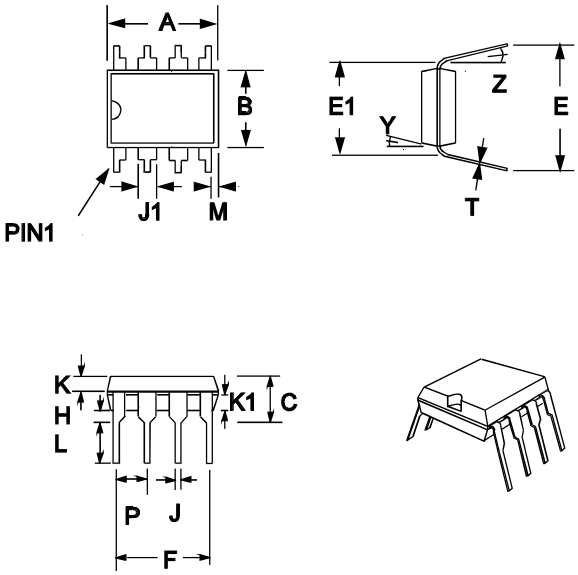
Xtal Frequency = 3.579545MHz, V_{DD} = 3.3V to 5.0V, Tamb = - 40°C to +85°C, S/N = 20dB.

		Notes	Min.	Typ.	Max.	Units
Signal Timings (ref. Figures 3, 4 and 5)						
t_I	Burst Length Ignored				70	ms
t_L	Burst Length Detected		145			ms
t_{GI}	Call Progress Tone Gap Length Ignored	7			20	ms
t_{GD}	Call Progress Tone Gap Length Detected	7	40			ms
t_{RP}	Call Progress Tone Response Time				145	ms
t_{DRP}	Call Progress Tone De-response Time				145	ms
t_{GDR}	Gap Detected Recorded	8	6			ms
t_{NG}	Non Call Progress Signal Gap Length Ignored	9		80		ms
t_{NRP}	Non Call Progress Signal Response Time		145			ms
t_{DNRP}	Non Call Progress signal De-response Time			80		ms
t_{CH}	State Change		0			ms

Notes:
7. Only applies to bursts of the same frequency.
8. To acknowledge a short tone gap ≥ 40 ms, No Signal is indicated for a minimum of 6ms.
9. If the gap > 90 ms, a No Signal state will be decoded.

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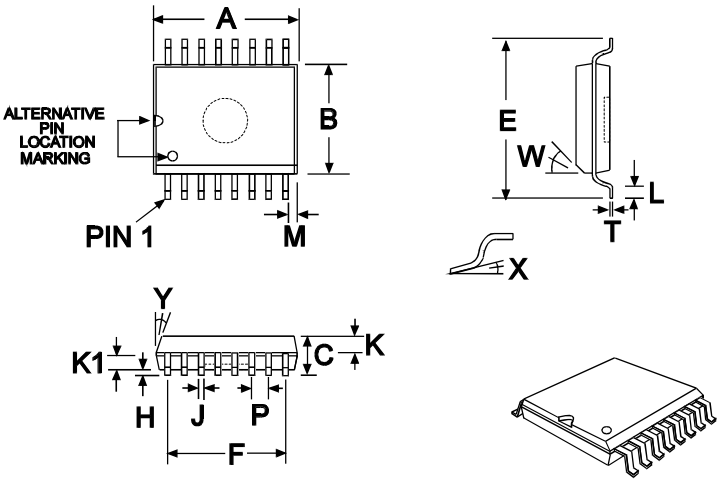
1.7.2 Packaging



DIM.	MIN.	TYP.	MAX.
A	0.346 (8.790)		0.400 (10.16)
B	0.240 (6.10)		0.260 (6.60)
C	0.145 (3.68)		0.187 (4.75)
E	0.300 (7.62)		0.370 (9.40).
E1	0.290 (7.37)		0.320 (8.13)
F		0.30 (7.62)	
H		0.030 (0.76)	
J	0.015 (0.38)		0.023 (0.58)
J1	0.045 (1.14)		0.065 (1.65)
K		0.062 (1.58)	
K1		0.062 (1.58)	
L	0.121 (3.07)		0.150 (3.81)
M		0.029 (0.74)	
P		0.100 (2.54)	
T	0.008 (0.20)		0.015 (0.38)
Y		7°	
Z		5°	

NOTE : All dimensions in inches (mm.)
Angles in degrees

Figure 6 P1 Mechanical Outline: *Order as part no. FX633P1*



DIM.	MIN.	TYP.	MAX.
A	0.395 (10.03)		0.413 (10.49)
B	0.291 (7.39)		0.299 (7.59)
C	0.093 (2.36)		0.105 (2.67)
E	0.394 (10.01)		0.419 (10.64)
F		0.366 (9.29)	
H	0.004 (0.10)		0.012 (0.30)
J	0.013 (0.33)		0.019 (0.48)
K		0.041 (1.04)	
K1		0.041 (1.04)	
L	0.016 (0.41)		0.050 (1.27)
M	0.021 (0.53)		0.031 (0.79)
P		0.050 (1.27)	
T	0.009 (0.23)		0.012 (0.30)
W		45°	
X	0°		8°
Y		7°	

NOTE : All dimensions in inches (mm.)
Angles in degrees

Figure 7 D4 Mechanical Outline: *Order as part no. FX633D4*

Handling precautions: This product includes input protection, however, precautions should be taken to prevent device damage from electro-static discharge. CML does not assume any responsibility for the use of any circuitry described. No IPR or circuit patent licences are implied. CML reserves the right at any time without notice to change the said circuitry and this product specification. CML has a policy of testing every product shipped using calibrated test equipment to ensure compliance with this product specification. Specific testing of all circuit parameters is not necessarily performed.

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